

Docket No.: P2000,0257

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : HEIKO FIBRANZ ET AL.

Filed : Concurrently herewith

Title : INTEGRATED CIRCUIT HAVING A SYNCHRONOUS AND AN  
ASYNCHRONOUS CIRCUIT AND METHOD FOR OPERATING  
SUCH AN INTEGRATED CIRCUIT

6 / IDS  
E. Willis  
4 - 18 - 02

11002 U.S. PTO  
10/033123



INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

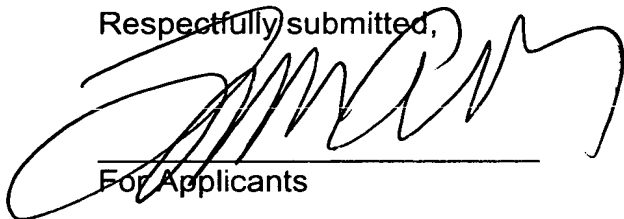
In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications  
are submitted herewith:

U.S. Patent 5,694,588 (Ohara et al.), dated December 2, 1997;

German translation of European Patent EP 0 400 734 B1 (Butel et al.), dated  
December 5, 1990, programmable binary signal delay procedure and use of an error  
correcting codes procedure;

If no translation of pertinent portions of any foreign language patents or publications  
mentioned above is included with the aforementioned copies of those applications,  
patents and/or publications, it is because no existing translation is readily available to  
the applicant.

Respectfully submitted,

  
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For Applicants

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Date: October 22, 2001

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